

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

<b>Applicant:</b>	<b>Yoshihisa MATSUBARA, et al.</b>	<b>Examiner:</b>	<b>Unassigned</b>
<b>Serial No.:</b>	<b>Unassigned</b>	<b>Group Art Unit:</b>	<b>Unassigned</b>
<b>Filed:</b>	<b>Herewith</b>	<b>Docket:</b>	<b>16782</b>
<b>For:</b>	<b>SEMICONDUCTOR APPARATUS OF WHICH RELIABILITY OF INTERCONNECTIONS IS IMPROVED AND MANUFACTURING METHOD OF THE SAME</b>		
	<b>Dated:</b>	<b>June 27, 2003</b>	

Commissioner for Patents  
P. O. Box 1450  
Alexandria, VA 22313-1450

**INFORMATION DISCLOSURE STATEMENT**

Sir:

In accordance with 37 C.F.R. §§ 1.97 and 1.98, it is requested that the following reference, which is also listed on the attached Form PTO-1449, be made of record in the above-identified case.

1. Japanese Patent Publication No. 2000-77413, dated March 14, 2000.

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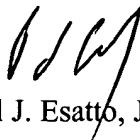
Dated: June 27, 2003

  
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Paul J. Esatto, Jr.

Applicant is submitting a copy of the above-cited reference. The relevance of the above-identified reference has been described in the specification.

Inasmuch as this Information Disclosure Statement is being submitted in accordance with the schedule set out in 37 C.F.R. § 1.97(b), no statement or fee is required.

Respectfully submitted,

A handwritten signature in black ink, appearing to read 'P. Esatto, Jr.', is positioned above the printed name.

Paul J. Esatto, Jr., Reg. No. 30,749

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